

FIG. 1

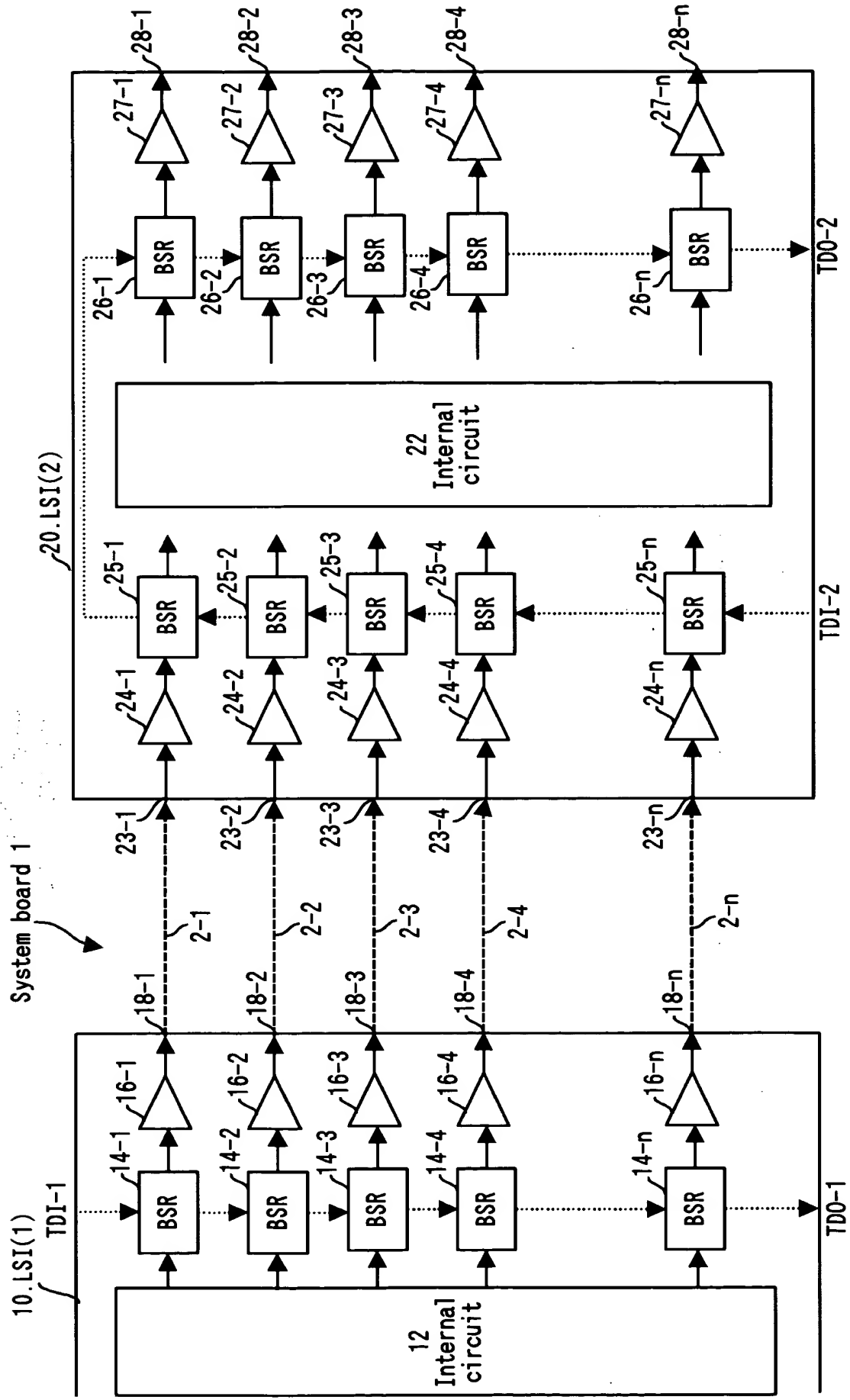


FIG. 2

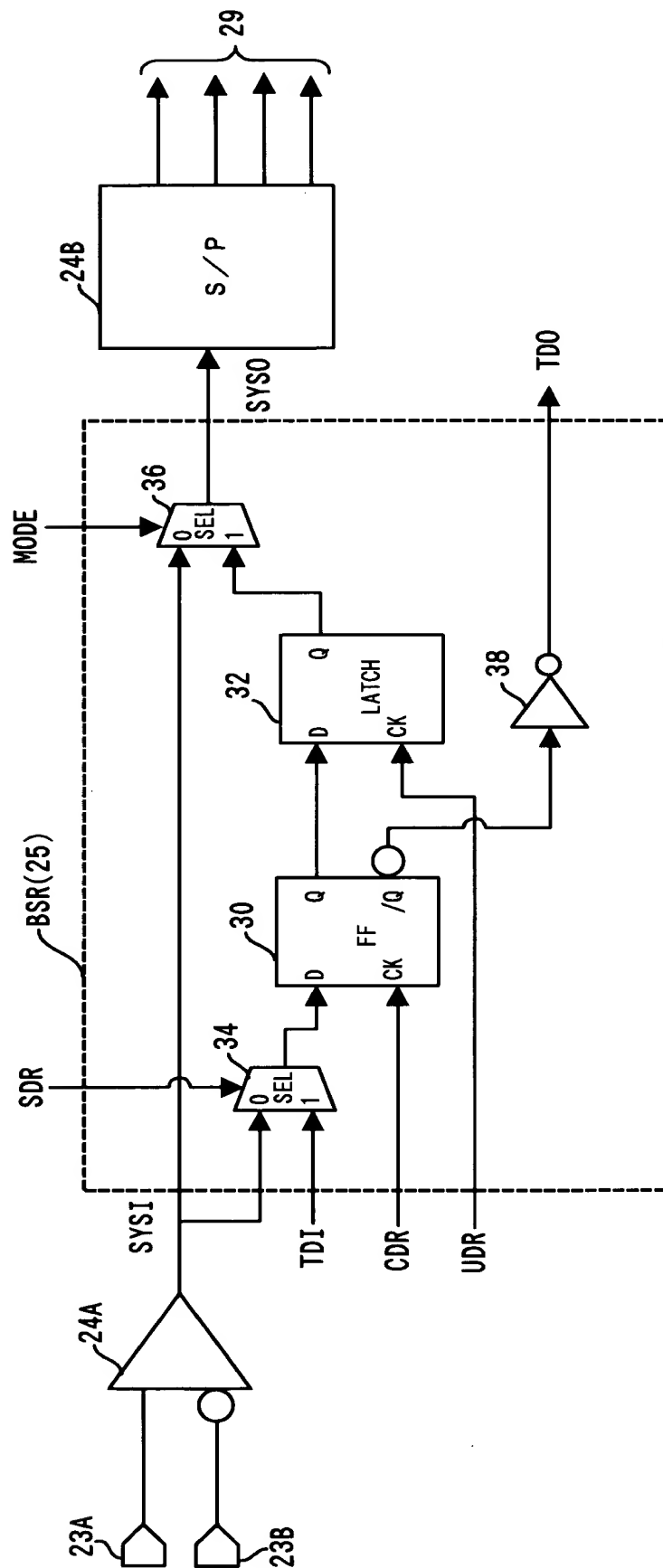


FIG. 3

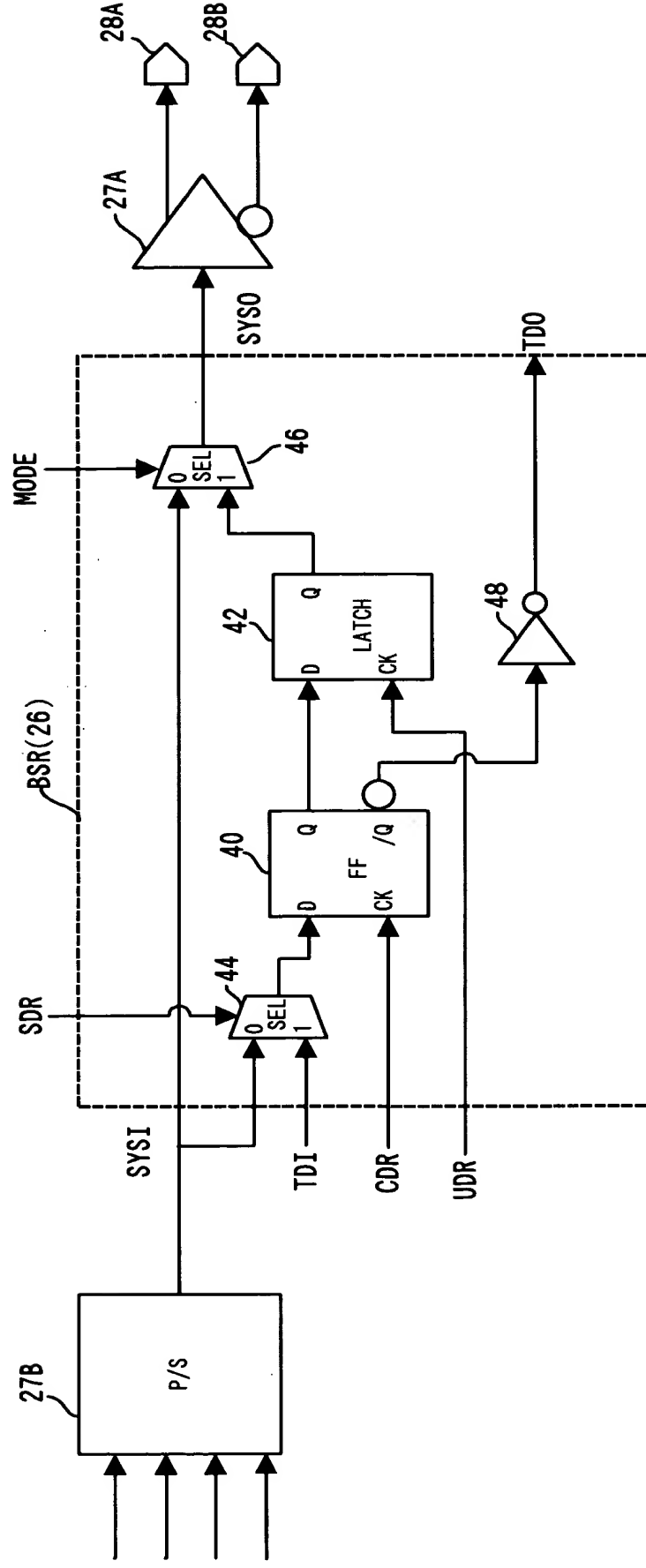


FIG. 4

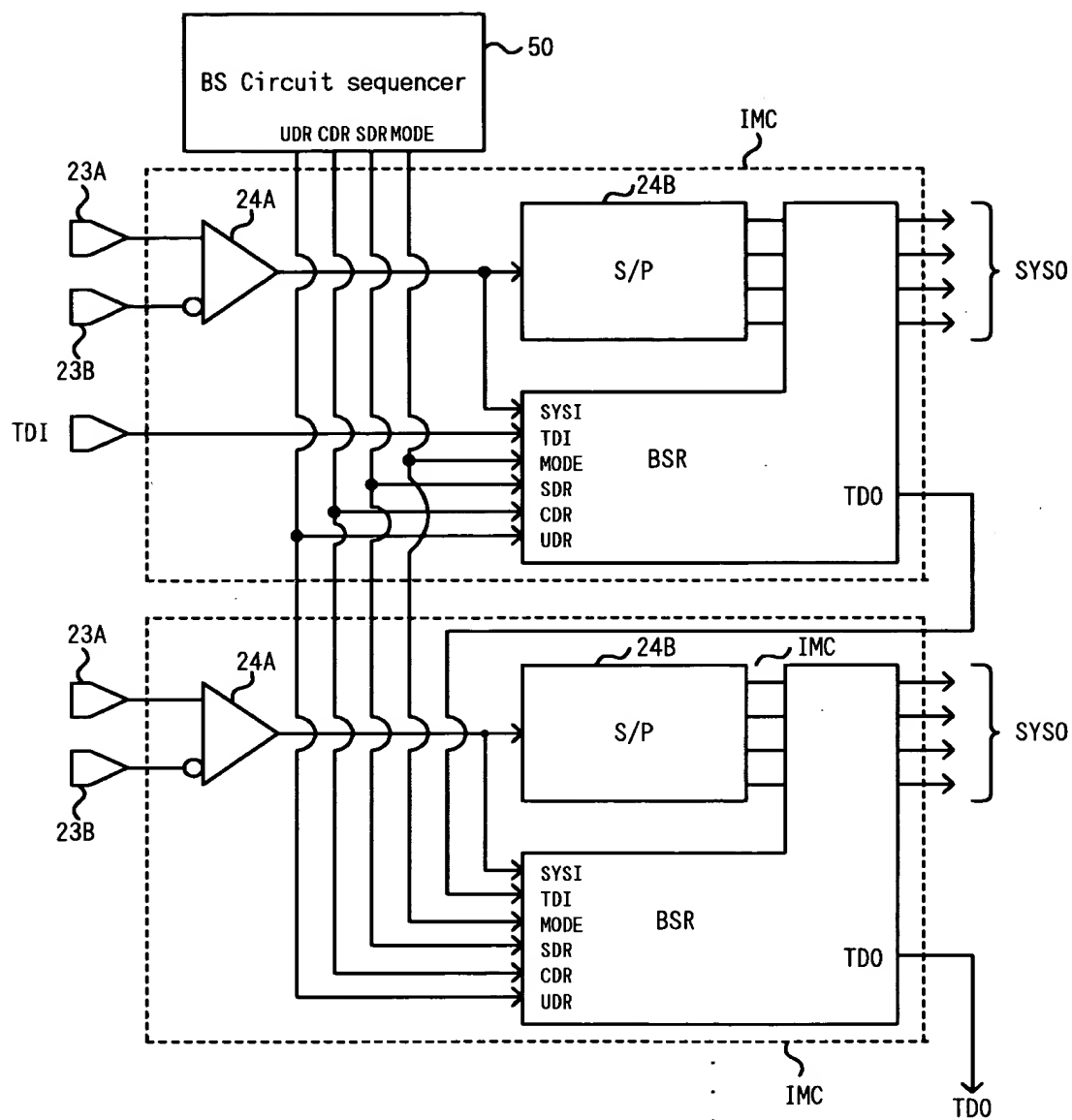


FIG. 5

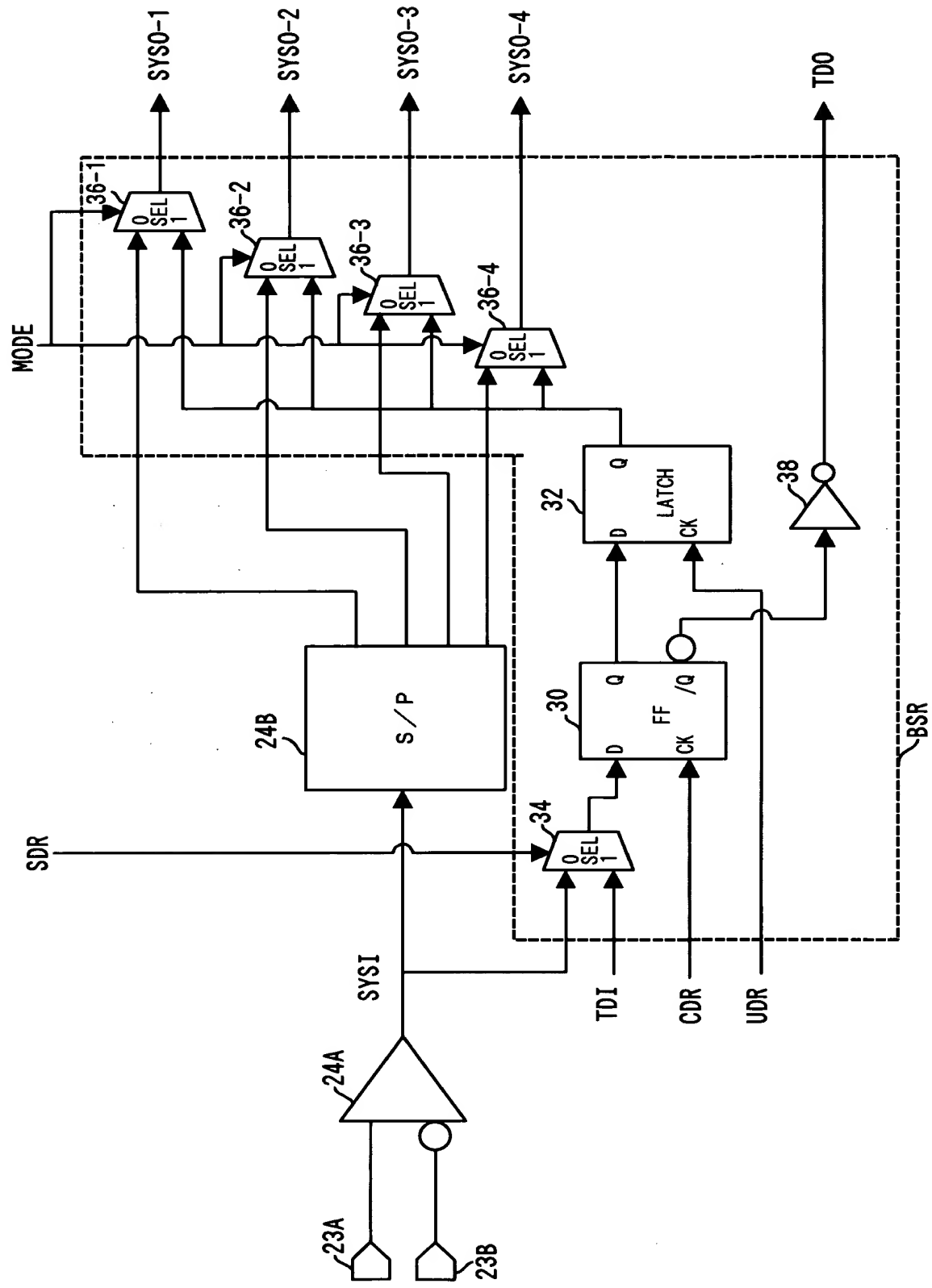


FIG. 6

Operation of BSR

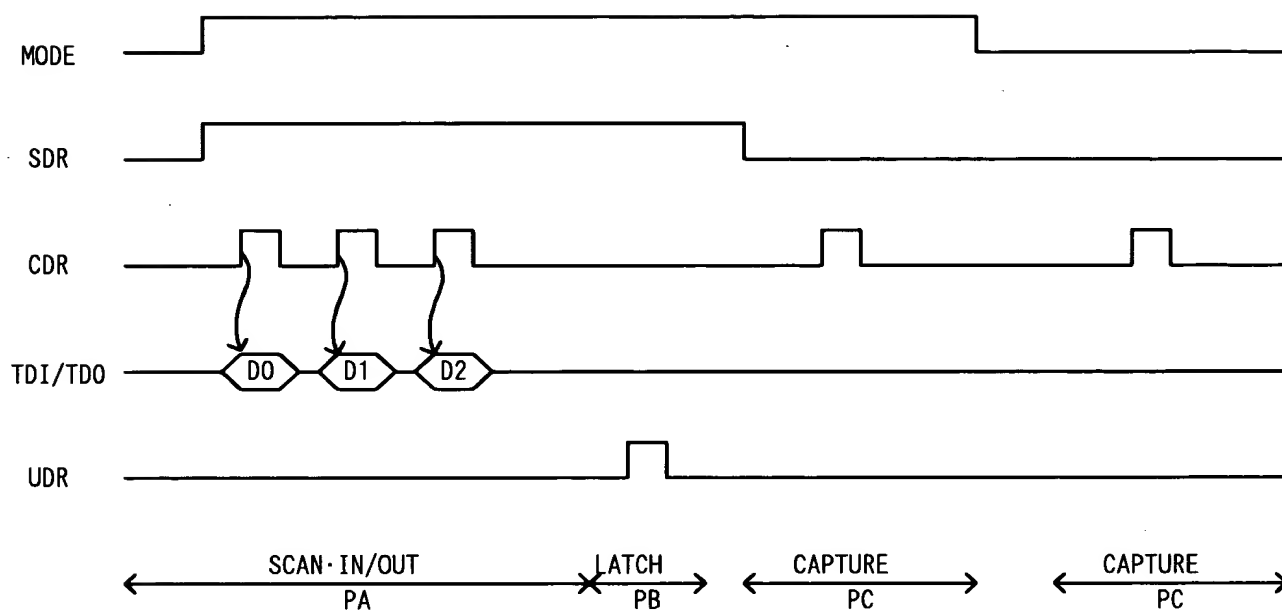


FIG. 7

Selector circuit

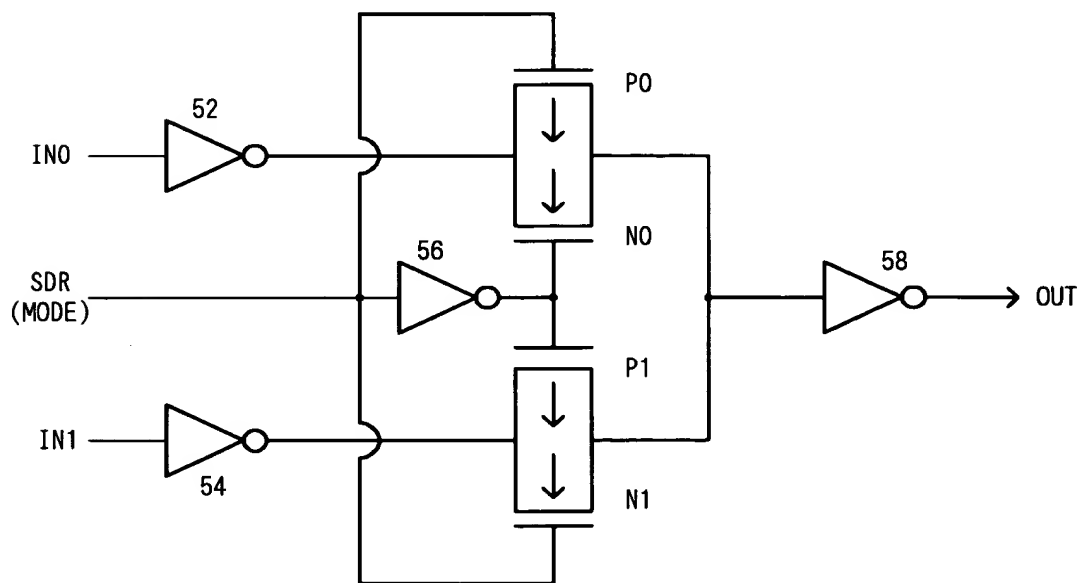


FIG. 8

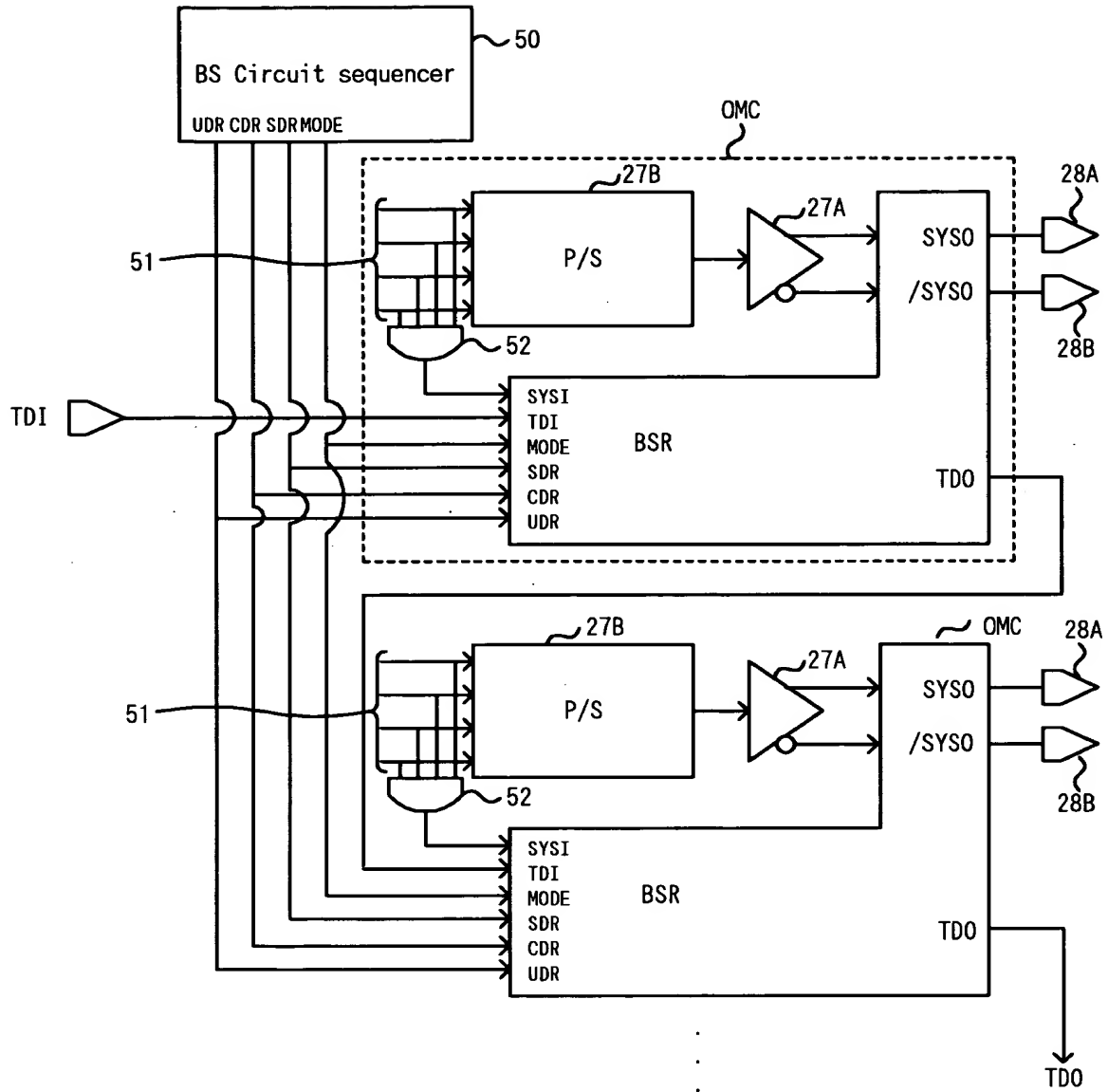


FIG. 9

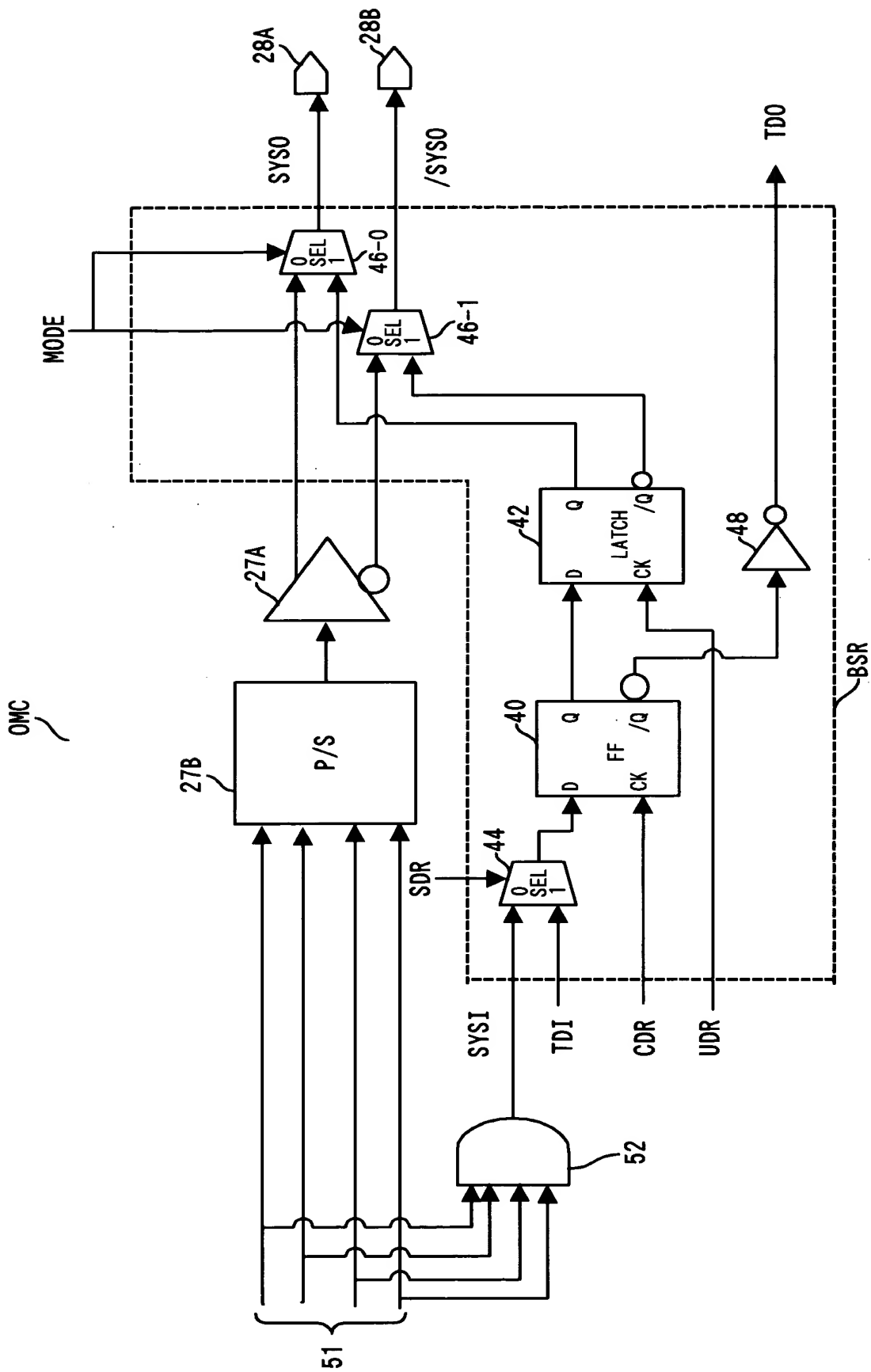


FIG. 10

Differential output buffer

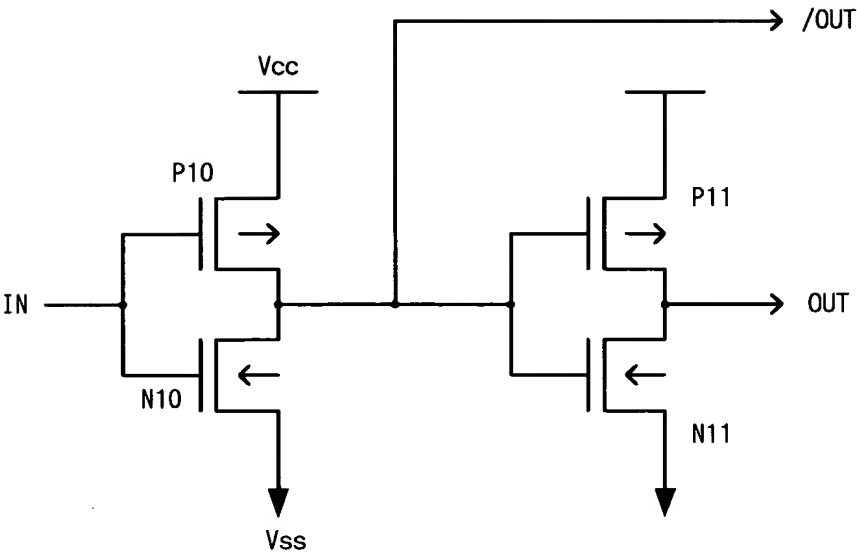


FIG. 10

FIG. 11

